**Alec Mabhiza Chirawu (亚历克上)**

**Report**

***Week 0***

I have been working on FPGA systems (F1 A Fast and Programmable Accelerator micro21\_fhe, CraterLake A Hardware Accelerator for Efficient craterlake). The main problem I have encountered is the missing of some hardware components to test the idea in these 2 papers.

I bought the components and they are coming.

***Week 1:***

I will be looking at Jung hee cheon papers , There are lot of papers focusing on different aspects and I will be reading Authentication papers which are familiar with the project I am working on.